

WHAT IS CLAIMED IS:

1. An apparatus comprising:
a processor, said processor adapted to:
 identify a grid of pixels having a predetermined
 pattern of pixel values;
 obtain predetermined pcode grid from memory for
 the identified grid of pixels; and
 send the predetermined pcode grid to controller
 circuit for printing.
2. The apparatus recited in claim 1 wherein said processor
is a PCODE processor.
3. The apparatus recited in claim 2 further comprising a
second processor adapted to format print data to pixel
data, the pixel data including pixel values.
4. The apparatus recited in claim 1 wherein said processor
is adapted to format print data to pixel data, the
pixel data including pixel values.
5. The apparatus recited in claim 1 wherein the grid of
pixels is a six-pixel by six-pixel grid.
6. The apparatus recited in claim 1 wherein the memory is
a register internal to said processor.
7. The apparatus recited in claim 1 wherein the memory is
non-volatile but re-writable memory.

8. The apparatus recited in claim 1 wherein the memory is external to said processor.
9. The apparatus recited in claim 1 wherein the predetermined pattern of pixel values and its corresponding pcode grid applies to a first color and a second predetermined p attern of pixel values and its corresponding pcode grid applies to a second color.
10. The apparatus recited in claim 1 wherein the apparatus is selected from a group consisting of a printer, a copier, and a multifunction appliance.
11. A method of printing data on a print medium, the method comprising:
 - identifying a grid of pixels having a predetermined pattern of pixel values;
 - obtaining predetermined pcode grid from memory for the identified grid of pixels; and
 - sending the predetermined pcode grid to controller circuit for printing.
12. The method recited in claim 11 further comprising formatting print data to pixel data, the pixel data including pixel values.
13. The method recited in claim 11 wherein the grid of pixels is a six-pixel by six-pixel grid.
14. The method recited in claim 11 wherein the memory is a register internal to said processor.

15. The method recited in claim 11 wherein the memory is non-volatile but re-writable memory.
16. The method recited in claim 11 wherein the memory is external to said processor.
17. The method recited in claim 11 wherein the predetermined pattern of pixel values and its corresponding pcode grid applies to a first color and a second predetermined pattern of pixel values and its corresponding pcode grid applies to a second color.